PW014N04ESL

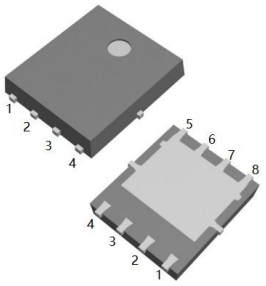
MOSFET

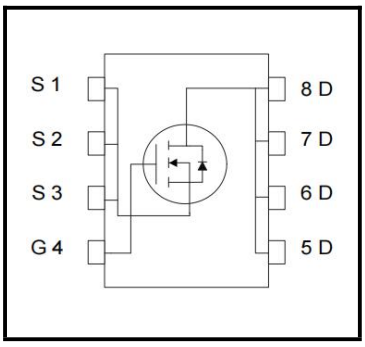
100 Amps,40 Volts N-CHANNEL MOSFET

FEATURE

- Low gate charge
- Low Ciss
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability
- RoHS 2.0 Compliant

DFN5*6





Parameter	Values	Unit
Bvdss	40	V
Id	100	A
Rdson(max)	1.4	mΩ

Ordering Code	Marking	Package	Packaging
PW014N04ESL	PW014N04ESL	DFN5*6	Tape and reel

Absolute Maximum Ratings(T_C=25℃,unless otherwise noted)

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Drain-Source Voltage	V_{DSS}	-	-	40	V	-
Gate-Source Voltage	V_{GS}	-20	-	20	V	-
Continuous Drain Current(Package Limited)	I_D	-	-	100	A	T_C=25℃
		-	-	60	A	T_C=100℃
Pulsed Drain Current(Note1)	I_{DM}	-	-	400	A	-
Single Pulse Avalanche Energy	E_{AS}	-	-	225	mJ	L=0.5mH,V_D=32V, T_C=25℃
Maximum Power Dissipation	P_D	-	-	114	W	T_C=25℃
Operating Junction and Storage Temperature Range	T_J,T_{STG}	-55	-	150	℃	-
Maximum lead temperature for soldering purposes, 1/8"from case for 5 seconds	T_L	-	-	260	℃	-

Thermal Characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Thermal resistance , Channel to Case	$R_{th(ch-c)}$	-	1.1	-	℃/W	-

Electrical Characteristics (T_C=25℃,unless otherwise noted)

Static characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Drain-Source Breakdown Voltage	BV_{DSS}	40	-	-	V	V_{GS}=0V,I_D=250μA
Zero Gate Voltage Drain Current	I_{DSS}	-	-	1	μA	V_{DS}=40V,V_{GS}=0V
Gate-Body Leakage Current,Forward	I_{GSSF}	-	-	100	nA	V_{GS}=20V,V_{DS}=0V
Gate-Body Leakage Current,Reverse	I_{GSSR}	-	-	-100	nA	V_{GS}=-20V,V_{DS}=0V
Gate-Source Threshold Voltage	$V_{GS(th)}$	1.0	-	2.5	V	V_{DS}=V_{GS},I_D=250μA
Drain-Source On-State Resistance	$R_{DS(on)}$	-	1.1	1.4	mΩ	V_{GS}=10V,I_D=20A
		-	1.7	2.3	mΩ	V_{GS}=4.5V,I_D=20A
Gate Resistance	R_g	-	2.7	-	Ω	V_{GS}=0V, V_{DS} Open, f=1MHz
Forward Transconductance	g_{fs}	-	12	-	S	V_{DS}=5V,I_D=20A

Dynamic characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Input Capacitance	C_{iss}	-	8302	-	pF	V_{DS}=25V,V_{GS}=0V,f=1.0MHZ
Output Capacitance	C_{oss}	-	1516	-	pF	V_{DS}=25V,V_{GS}=0V,f=1.0MHZ
Reverse Transfer Capacitance	C_{rss}	-	131	-	pF	V_{DS}=25V,V_{GS}=0V,f=1.0MHZ
Turn-On Delay Time	$t_{d(on)}$	-	/	-	ns	V_{DD}=20V,R_G=10Ω,V_{GS}=10V,R_L=1Ω
Turn-On Rise Time	t_r	-	/	-	ns	V_{DD}=20V,R_G=10Ω,V_{GS}=10V,R_L=1Ω
Turn-Off Delay Time	$t_{d(off)}$	-	/	-	ns	V_{DD}=20V,R_G=10Ω,V_{GS}=10V,R_L=1Ω
Turn-Off Fall Time	t_f	-	/	-	ns	V_{DD}=20V,R_G=10Ω,V_{GS}=10V,R_L=1Ω

Gate charge characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Total Gate Charge	Q_g	-	127.4	-	nC	V_{DS}=32V,I_D=20A,V_{GS}=10V
Gate-Source Charge	Q_{gs}	-	34.8	-	nC	V_{DS}=32V,I_D=20A,V_{GS}=10V
Gate-Drain Charge	Q_{gd}	-	25.9	-	nC	V_{DS}=32V,I_D=20A,V_{GS}=10V

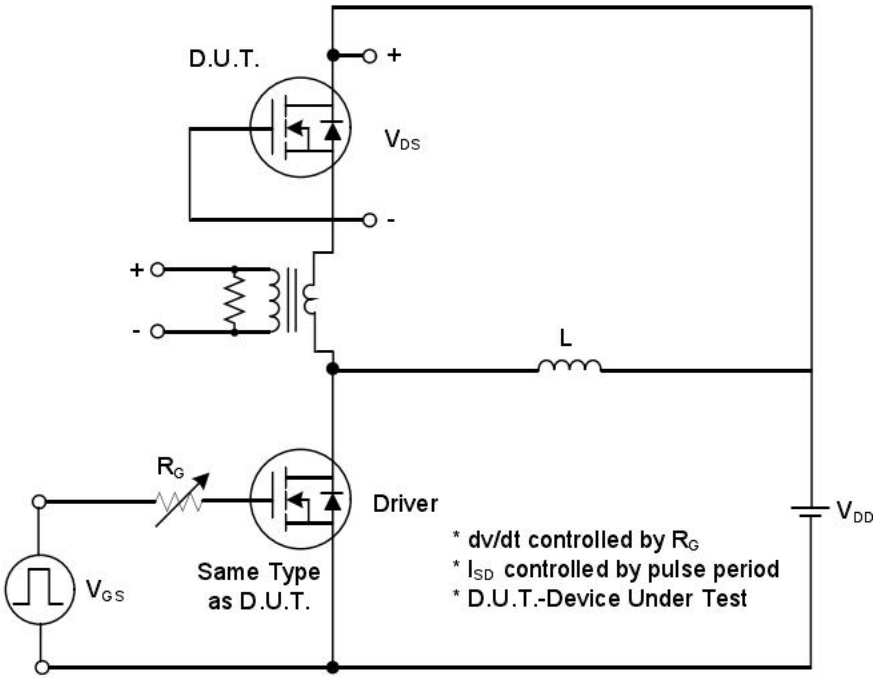
Reverse diode

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Continuous Diode Forward Current	I_S	-	-	100	A	-
Pulsed Diode Forward Current	I_{SM}	-	-	400	A	-
Diode Forward Voltage	V_{SD}	-	-	1.2	V	I_S=20A,V_{GS}=0V
Reverse Recovery Time	t_{rr}	-	100	-	ns	V_D=30V,I_F=1A
Reverse Recovery Charge	Q_{rr}	-	163	-	nC	di/dt=100A/μs,(Note2)

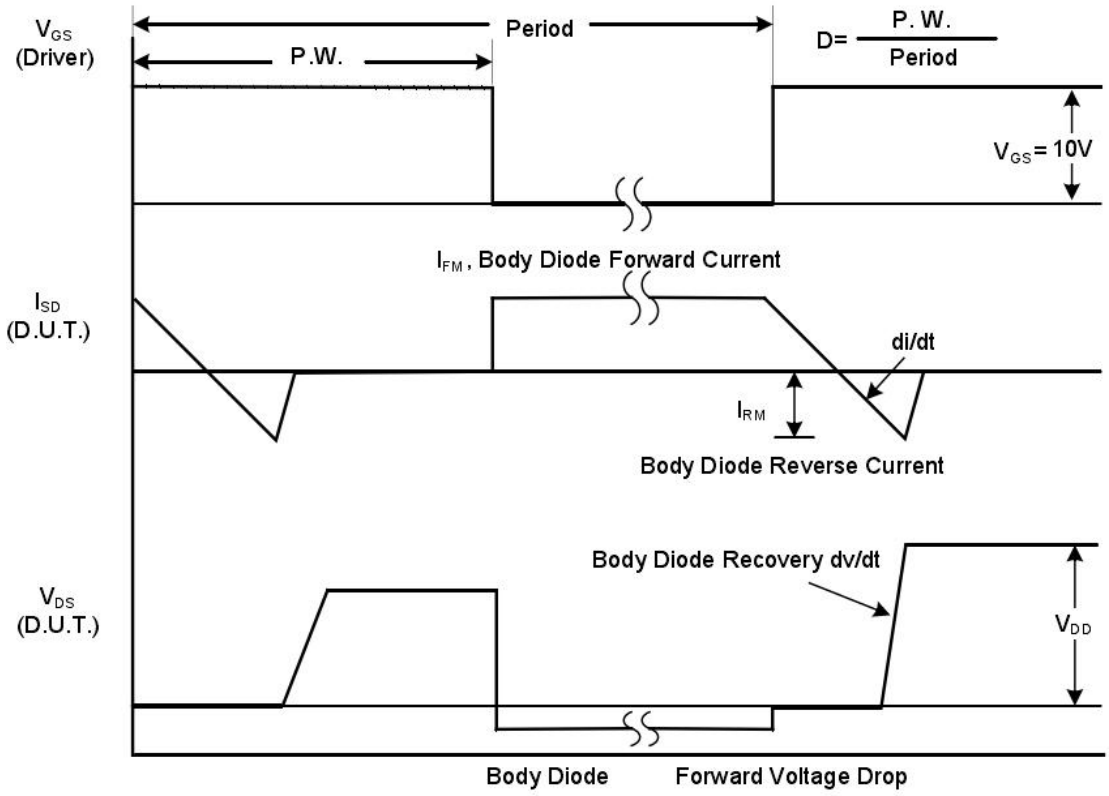
Notes

1. Repetitive Rating:pulse width limited by maximum junction temperature.
2. Pulse width≤300us,duty cycle≤2%.

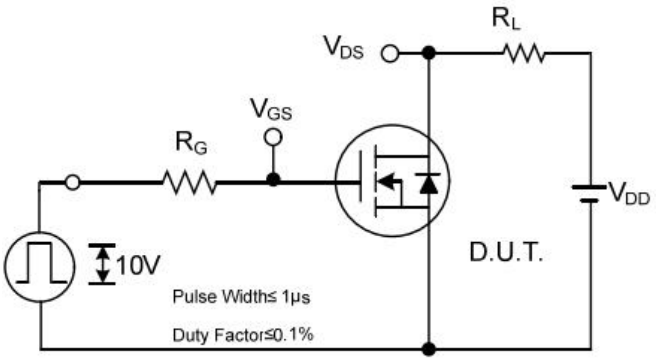
RATING AND CHARACTERISTIC CURVES



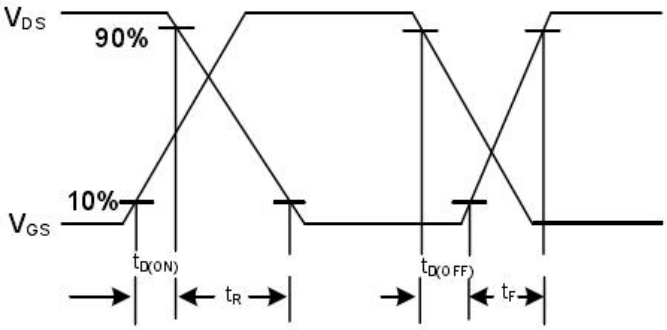
Peak Diode Recovery dv/dt Test Circuit



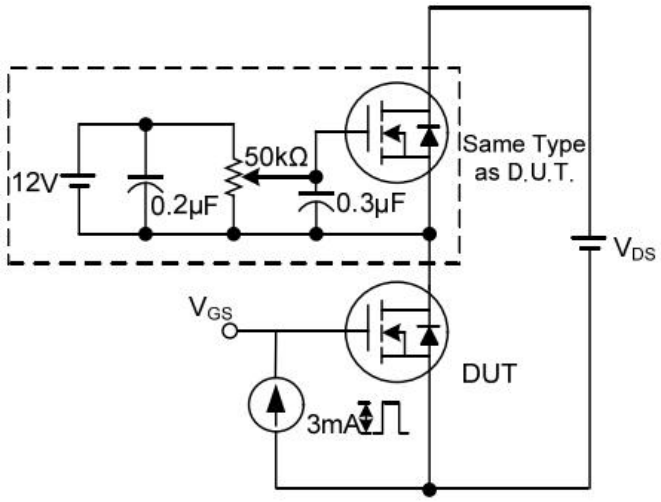
Peak Diode Recovery dv/dt Waveforms



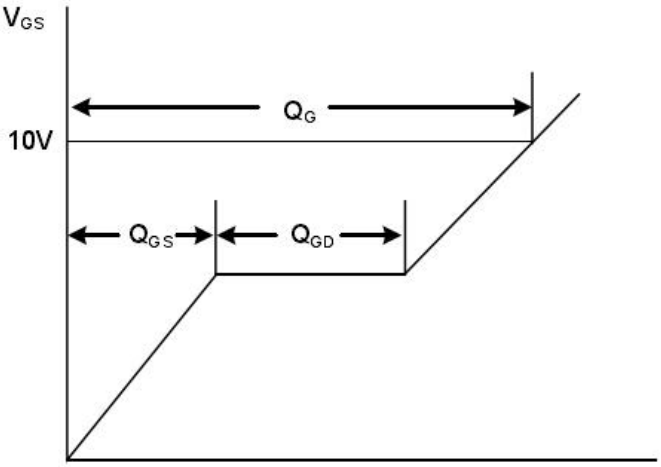
Switching Test Circuit



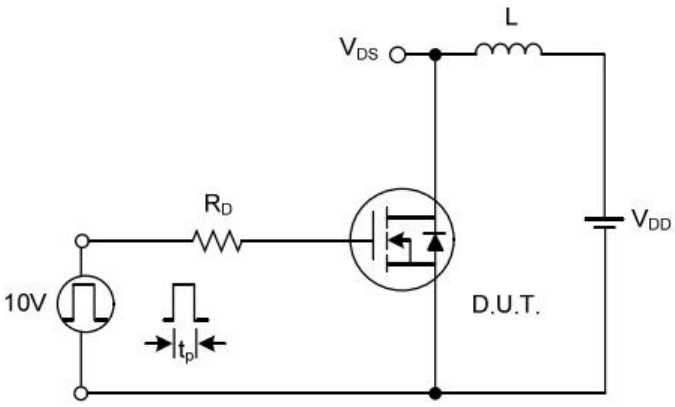
Switching Waveforms



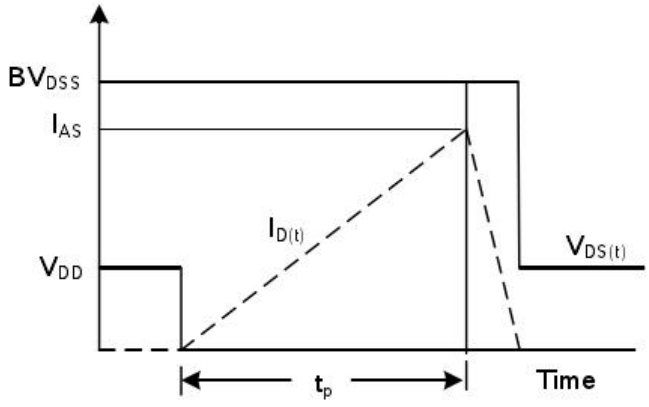
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

RATING AND CHARACTERISTIC CURVES

Figure.1 Typical Output Characteristics

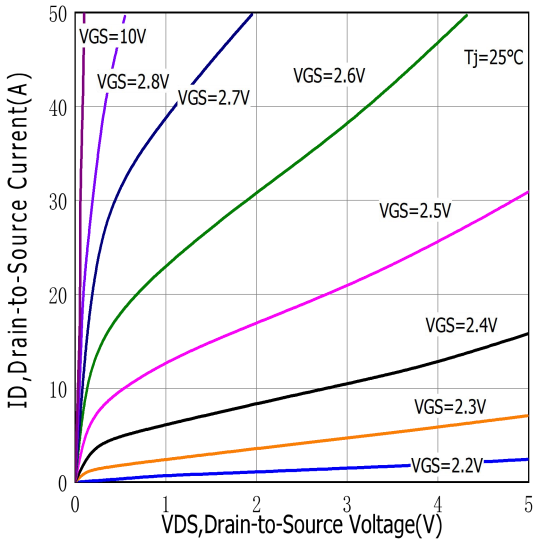


Figure.2 Typical Gate Charge vs Gate to Source Voltage

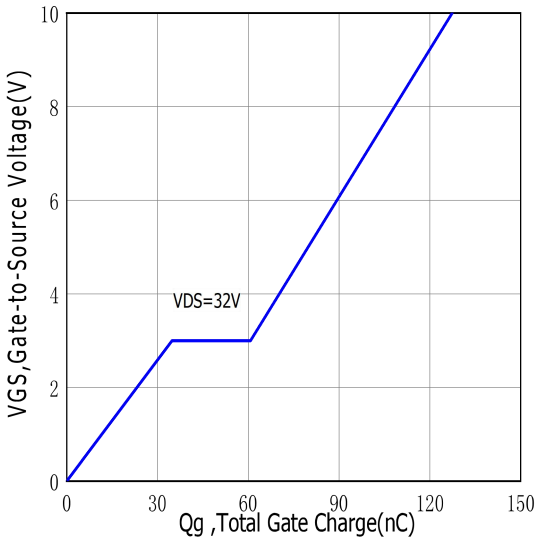


Figure.3 Typical Body Diode Transfer Characteristics

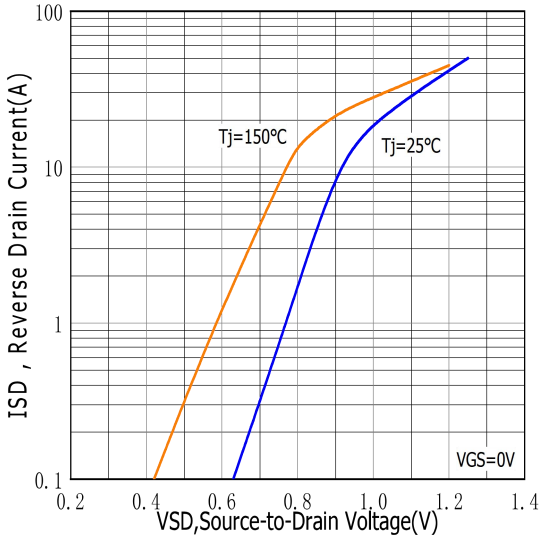


Figure.4 Typical Capacitance vs Drain to Source Voltage

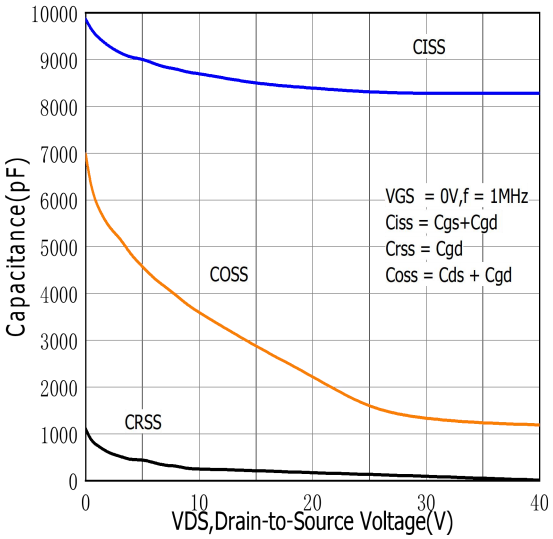


Figure.5 Typical Breakdown Voltage vs Junction Temperature

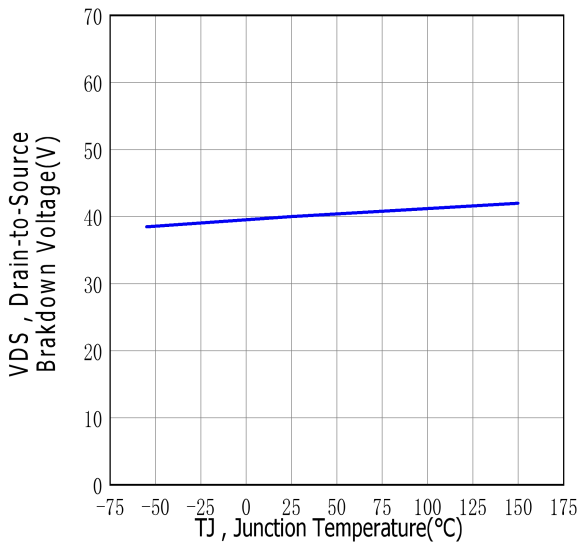


Figure.6 Typical Drain to Source on Resistance vs Junction Temperature

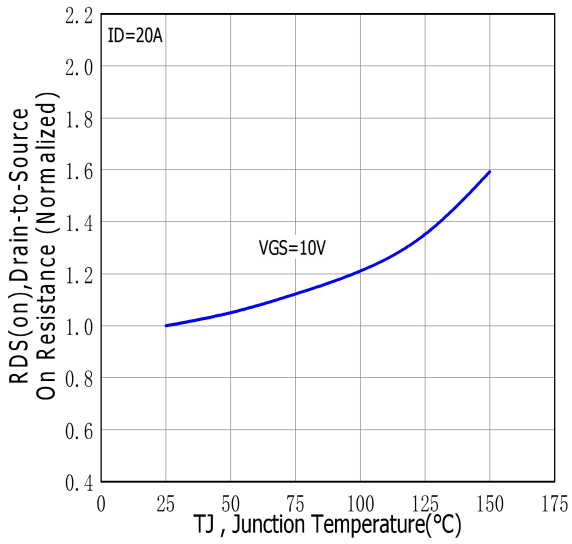


Figure.7 Maximum Forward Bias Safe Operating Area

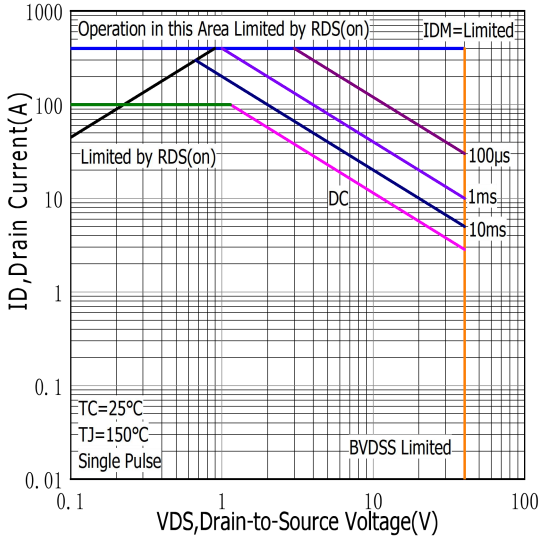


Figure.8 Typical Drain to Source ON Resistance vs Drain Current

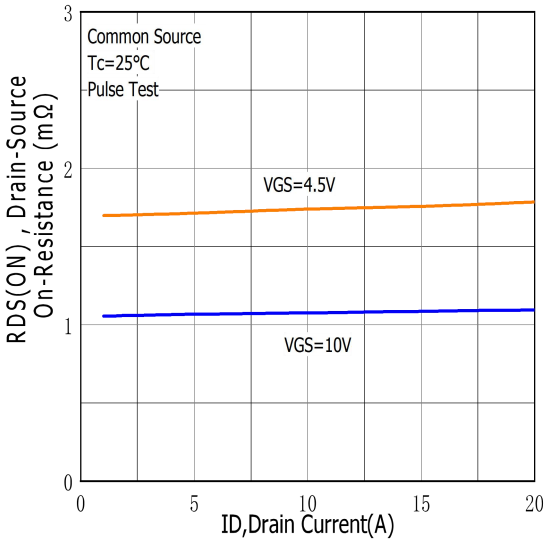


Figure.9 Maximum EAS vs Channel Temperature

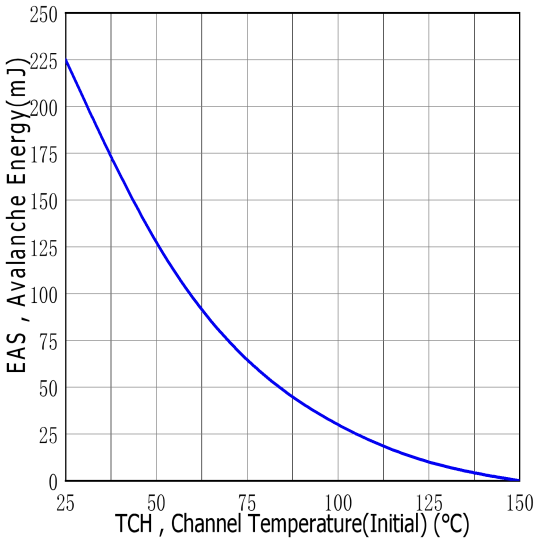


Figure.10 Typical Threshold Voltage vs Case Temperature

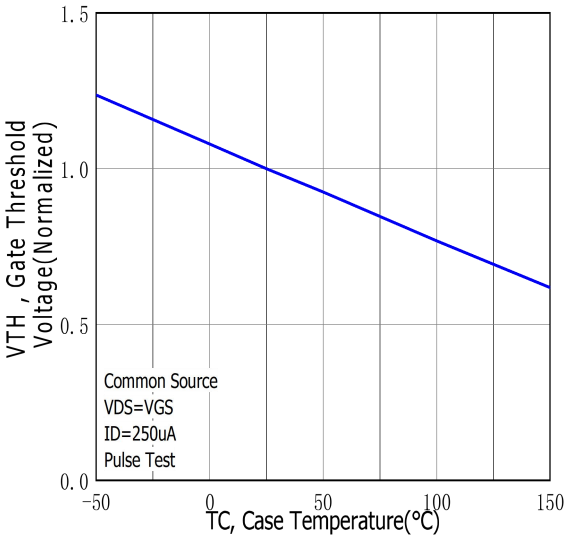


Figure.11 Typical Transfer Characteristics

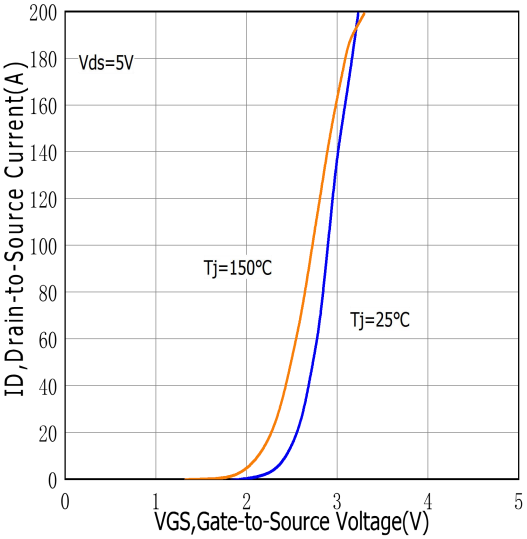


Figure.12 Maximum Power Dissipation vs Case Temperature

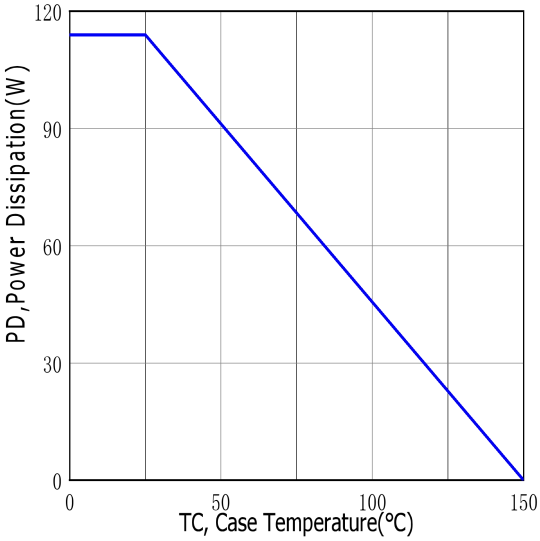
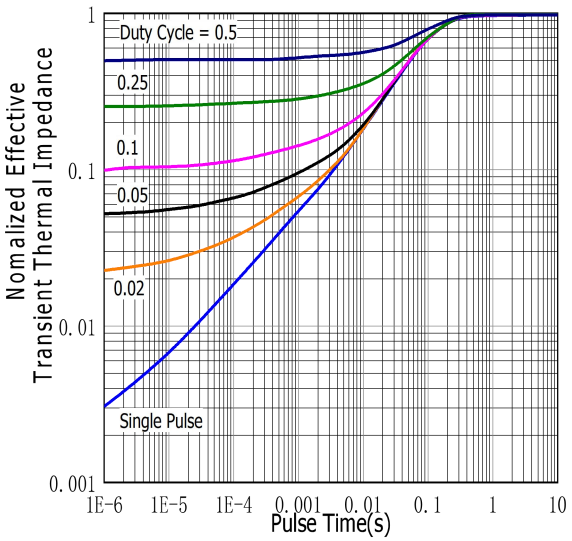


Figure.13 Maximum Effective Thermal Impedance , Junction to Case



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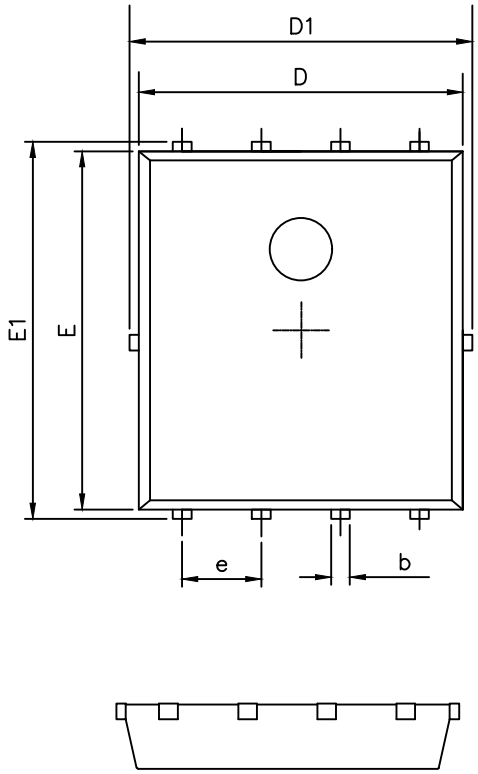
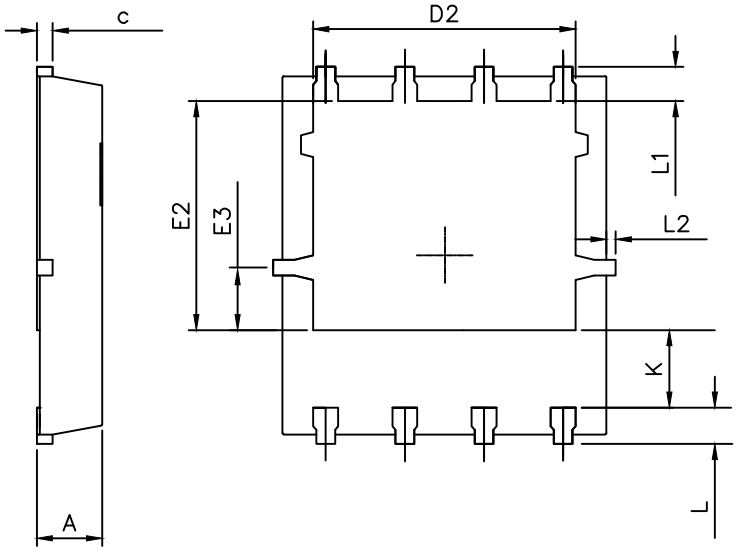
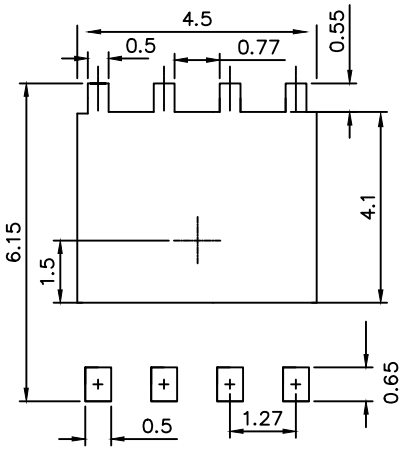
CHONGQING PINGWEI ENTERPRISE CO.,LTD

DFN5x6 PACKAGE OUTLINE





RECOMMENDED LAND PATTERN



UNIT: mm

	MIN	NOM	MAX
A	0.90	1.00	1.10
b	0.25	0.35	0.50
c	0.10	0.20	0.30
D	4.80	5.00	5.30
D1	4.90	5.10	5.50
D2	3.92	4.02	4.20
E	5.65	5.75	5.85
E1	5.90	6.05	6.20
E2	3.325	3.525	3.775
E3	0.80	0.90	1.00
e		1.27	
L	0.40	0.55	0.70
L1		0.65	
L2	0.00		0.15
K	1.00	1.30	1.50