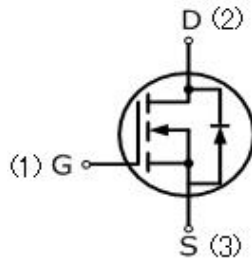


## 7N80F

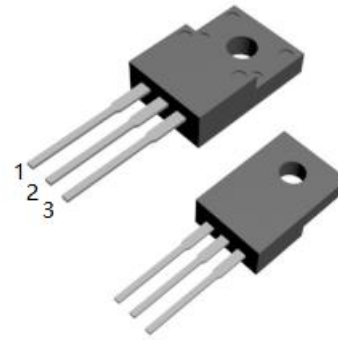
### 7 Amps,800 Volts N-CHANNEL Power MOSFET

#### FEATURE

- 7A,800V, $R_{DS(ON)MAX}=1.8\Omega@V_{GS}=10V/3.5A$
- Low gate charge
- Low  $C_{iss}$
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability
- Halogen free



#### TO-220F-3L



#### Absolute Maximum Ratings ( $T_C=25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	7N80F	UNIT
Drain-Source Voltage	$V_{DSS}$	800	V
Gate-Source Voltage	$V_{GSS}$	$\pm 30$	
Continuous Drain Current	$I_D$	7	A
Pulsed Drain Current(Note1)	$I_{DM}$	28	
Single Pulse Avalanche Energy (Note 2)	$E_{AS}$	256	mJ
Reverse Diode dV/dt (Note 3)	dv/dt	5	V/ns
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55to+150	$^\circ\text{C}$
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	$T_L$	260	$^\circ\text{C}$

Parameter	Symbol	7N80F	Units
Thermal resistance , Channel to Case	$R_{th(ch-c)}$	3.125	$^\circ\text{C}/\text{W}$
Thermal resistance , Channel to Ambient	$R_{th(ch-a)}$	100	$^\circ\text{C}/\text{W}$
Maximum Power Dissipation	$T_C=25^\circ\text{C}$ $P_D$	40	W

Electrical Characteristics (T <sub>c</sub> =25°C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	800	—	—	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =800V, V <sub>GS</sub> =0V	—	—	1	uA
Gate-Body Leakage Current, Forward	I <sub>GSSF</sub>	V <sub>GS</sub> =30V, V <sub>DS</sub> =0V	—	—	100	nA
Gate-Body Leakage Current, Reverse	I <sub>GSSR</sub>	V <sub>GS</sub> =-30V, V <sub>DS</sub> =0V	—	—	-100	nA
<b>On Characteristics</b>						
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	2	—	4	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =3.5A	—	1.5	1.8	Ω
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V,	—	1350	—	pF
Output Capacitance	C <sub>oss</sub>	f=1.0MHZ	—	120	—	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		—	12	—	pF
<b>Switching Characteristics</b>						
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =400V, I <sub>D</sub> =7A,	—	15	—	ns
Turn-On Rise Time	t <sub>r</sub>	R <sub>G</sub> =12Ω (Note3,4)	—	25	—	ns
Turn-Off Delay Time	t <sub>d(off)</sub>		—	50	—	ns
Turn-Off Fall Time	t <sub>f</sub>		—	30	—	ns
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =400V, I <sub>D</sub> =7A,	—	34	—	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>GS</sub> =0 to 10V, (Note3,4)	—	6	—	nC
Gate-Drain Charge	Q <sub>gd</sub>		—	14	—	nC
<b>Drain-Source Body Diode Characteristics and Maximum Ratings</b>						
Continuous Diode Forward Current	I <sub>S</sub>		—	—	7	A
Pulsed Diode Forward Current	I <sub>SM</sub>		—	—	28	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =7A, V <sub>GS</sub> =0V	—	—	1.5	V
Reverse Recovery Time	t <sub>rr</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =7A,	—	185	—	ns
Reverse Recovery Charge	Q <sub>rr</sub>	dI <sub>F</sub> /dt=100A/us, (Note4)	—	0.85	—	uC

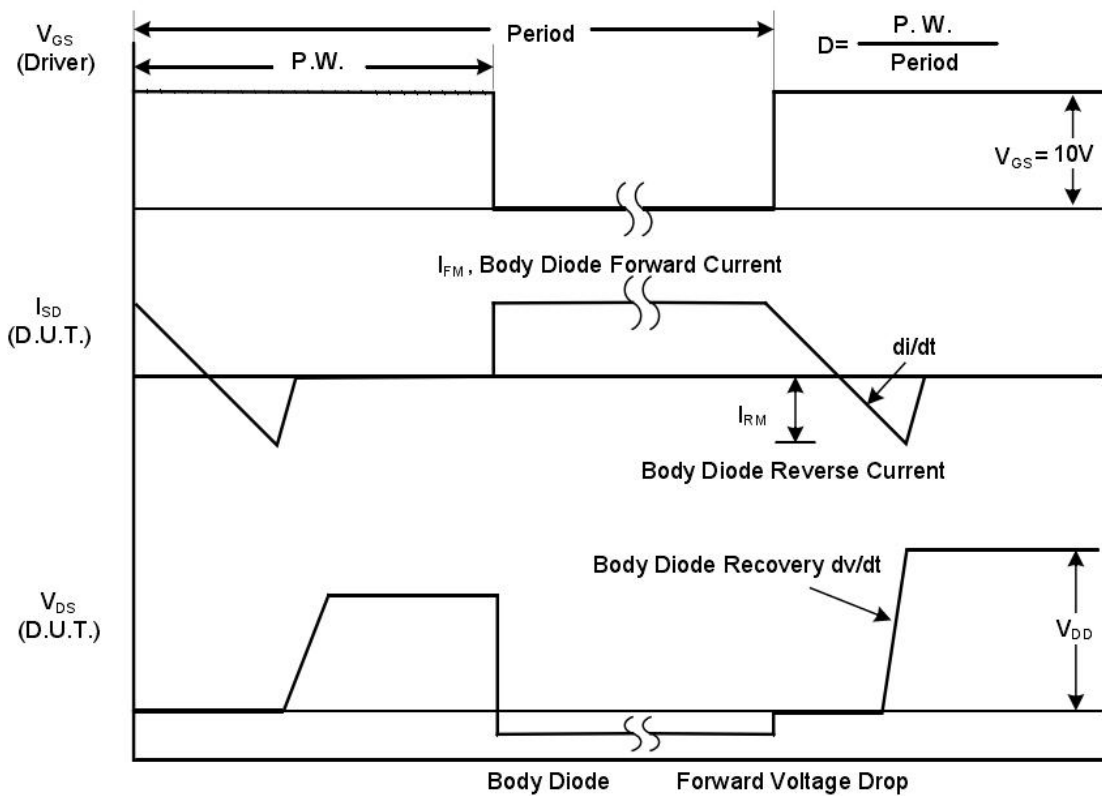
**Notes**

1. Repetitive Rating: pulse width limited by maximum junction temperature.
2. V<sub>DD</sub>=50V, L=10mH, R<sub>g</sub>=25Ω, starting T<sub>J</sub>=25°C.
3. I<sub>SD</sub> ≤ I<sub>D</sub>, dI/dt ≤ 100A/us, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, starting T<sub>J</sub>=25°C, Pulse width ≤ 300us; duty cycle ≤ 2%.
4. Repetitive rating; pulse width limited by maximum junction temperature.

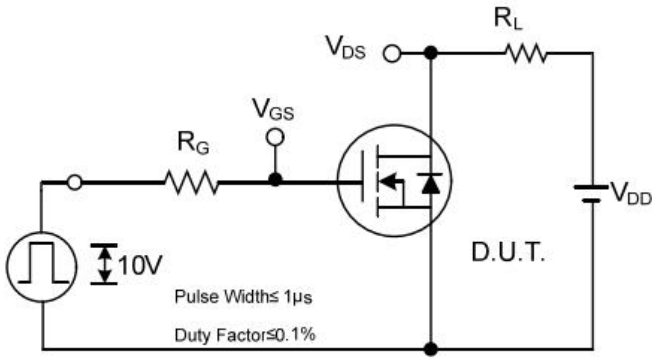
**TEST CIRCUIT AND WAVEFORM**



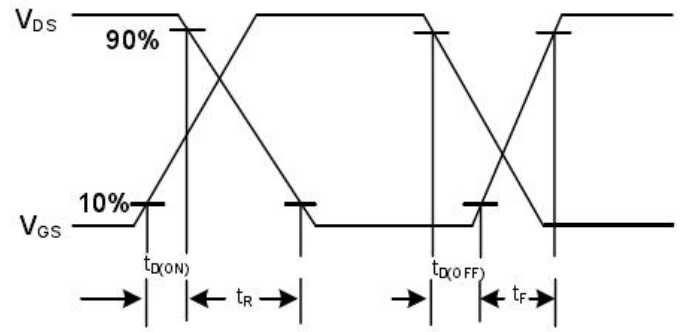
**Peak Diode Recovery dv/dt Test Circuit**



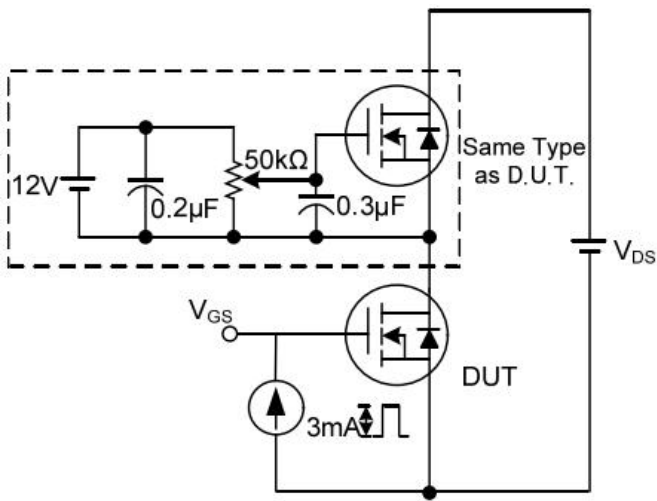
**Peak Diode Recovery dv/dt Waveforms**



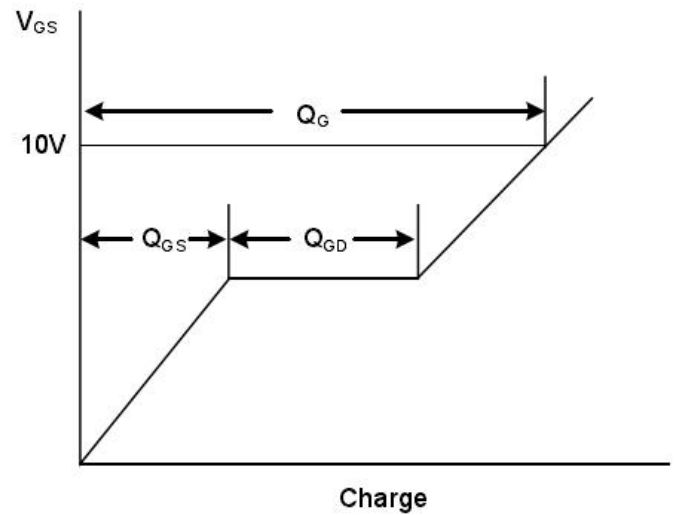
**Switching Test Circuit**



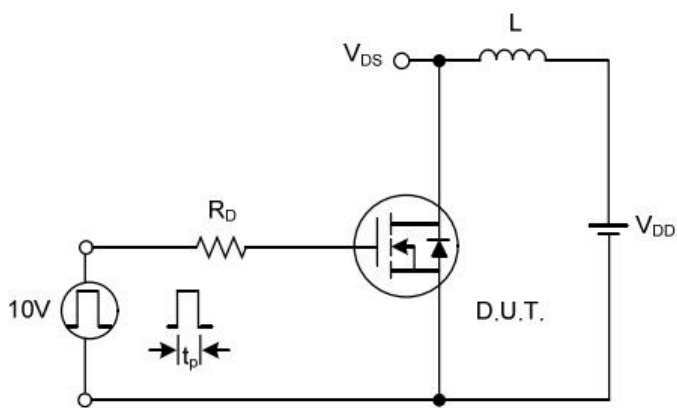
**Switching Waveforms**



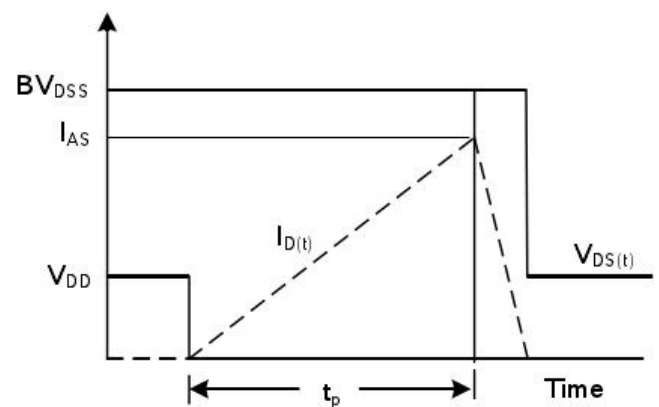
**Gate Charge Test Circuit**



**Gate Charge Waveform**

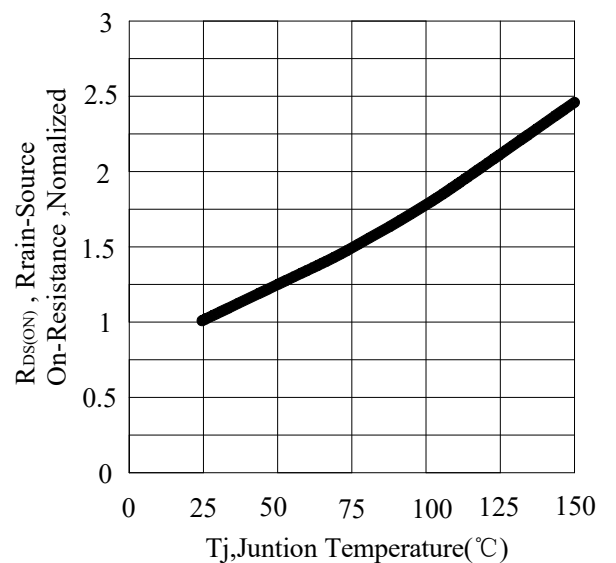
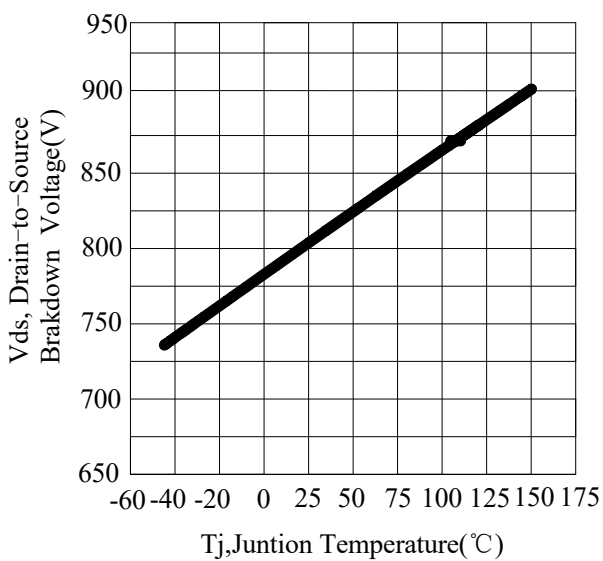
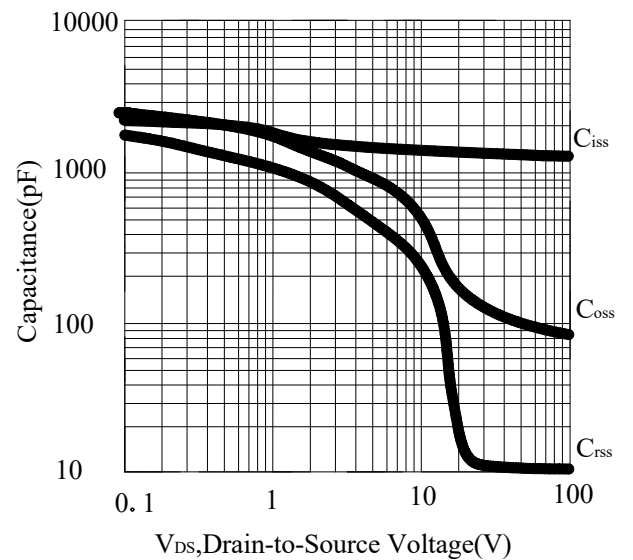
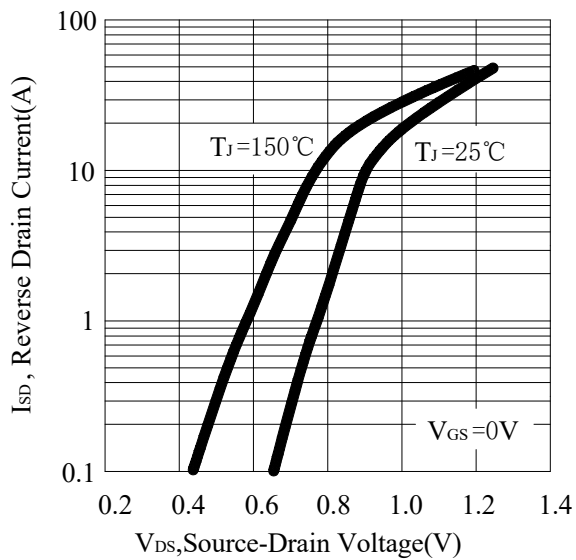
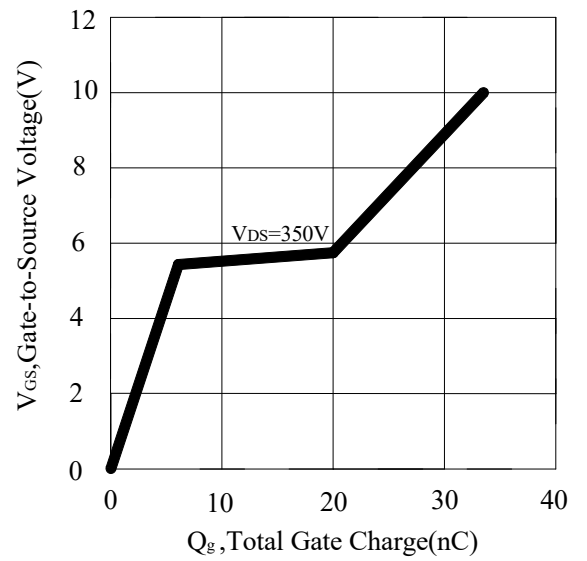
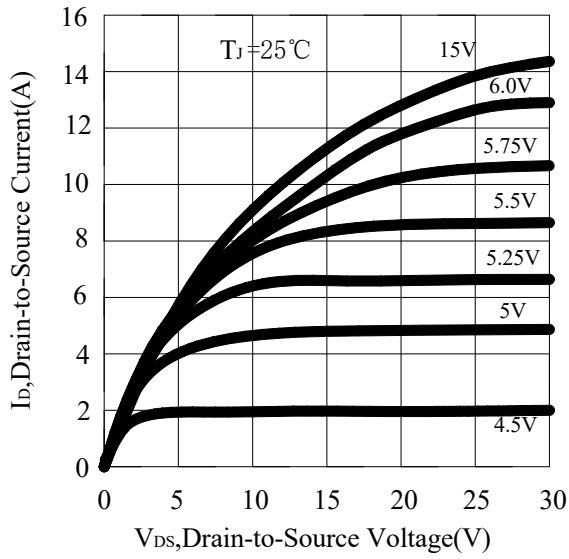


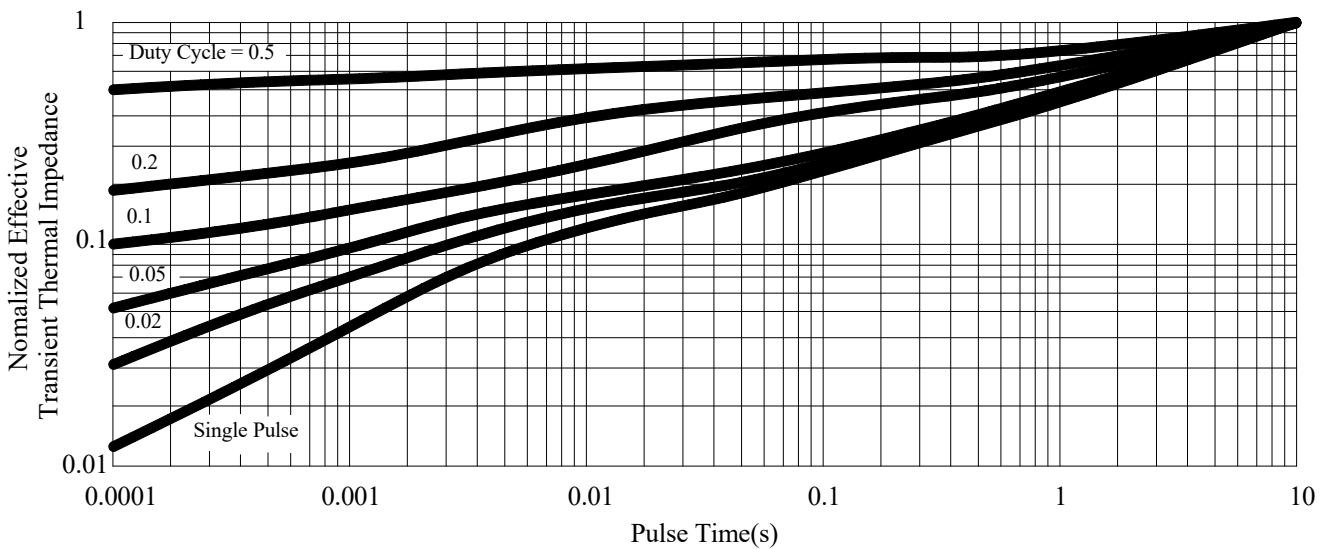
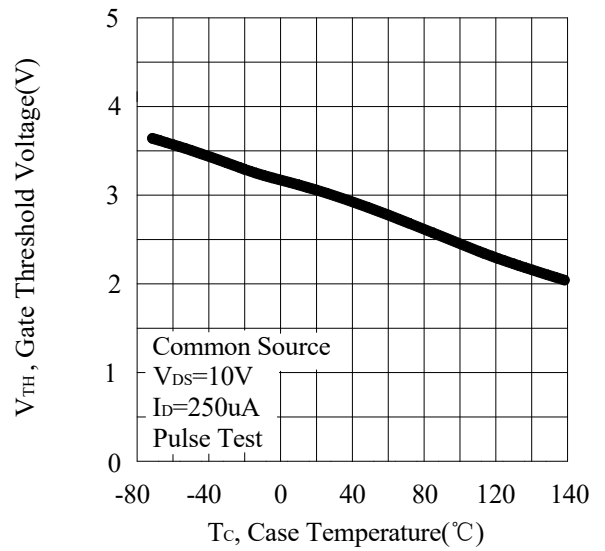
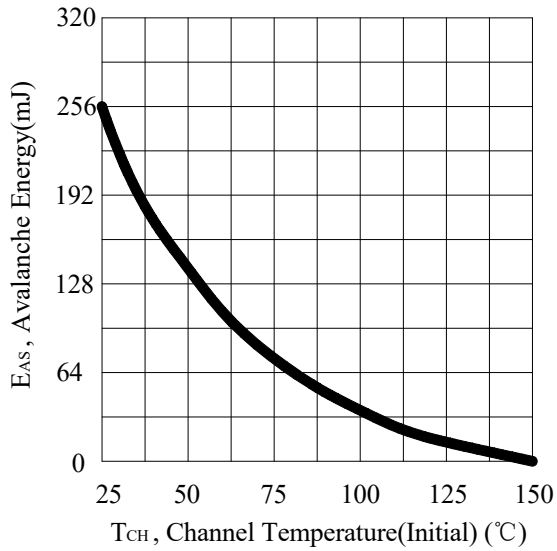
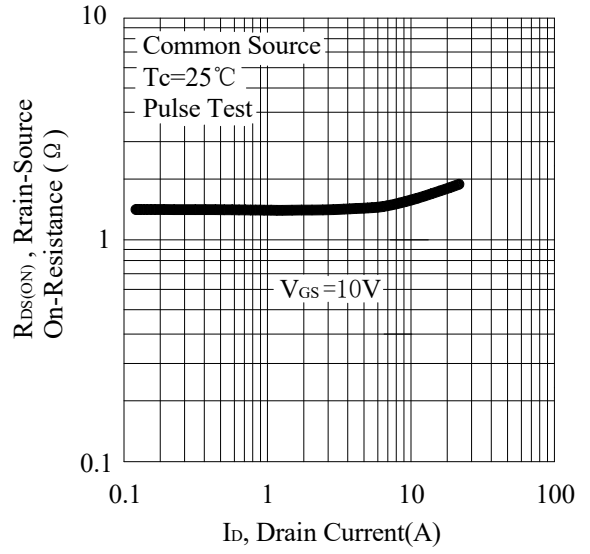
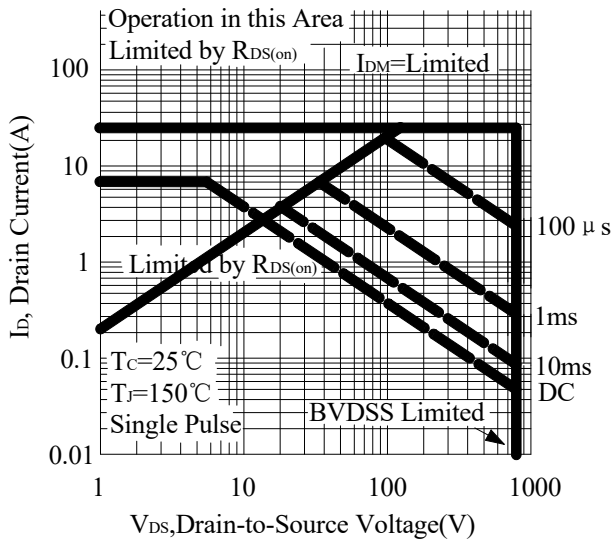
**Unclamped Inductive Switching Test Circuit**



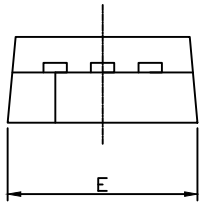
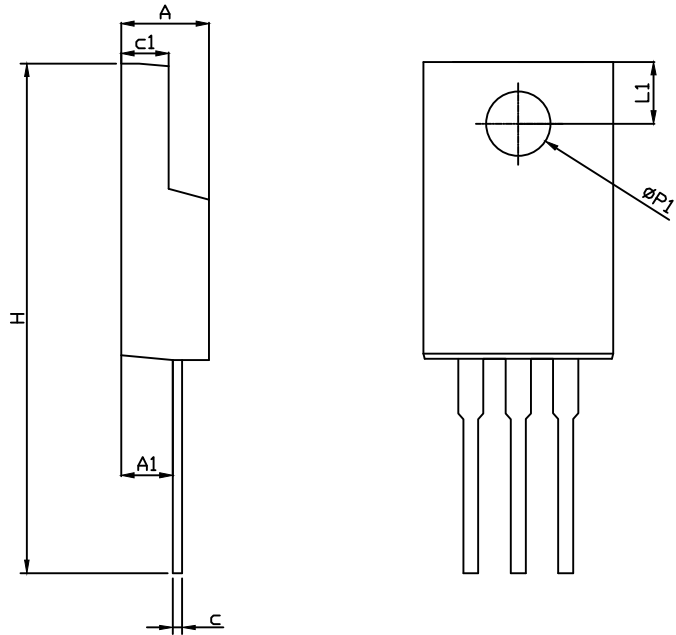
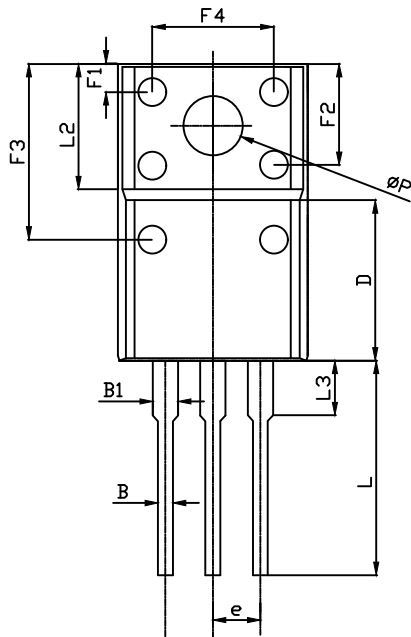
**Unclamped Inductive Switching Waveforms**

## RATING AND CHARACTERISTIC CURVES

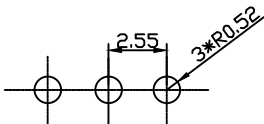




# TO-220F-3L PACKAGE OUTLINE



RECOMMENDED LAND PATTERN



UNIT: mm

	MIN	NOM	MAX
A	4.40	4.60	4.80
A1	2.63	2.76	2.89
B	0.75	0.80	0.90
B1	1.12	1.27	1.42
c	0.40	0.50	0.60
c1	2.60	2.70	2.80
D	7.50	7.80	8.10
e	-	2.55REF	-
E	9.86	10.00	10.10
F1	1.90	2.12	2.40
F2	5.00	5.30	5.65
F3	8.70	9.00	9.30
F4	6.20	6.50	6.80
H	27.80	28.30	28.80
L	13.10	13.30	13.50
L1	2.85	3.00	3.15
L2	-	6.70REF	-
L3	3.10	3.60	4.10
<b>ΦP</b>	3.00	3.30	3.60
ΦP1	2.80	3.10	3.40